Faculty Profile

Name : R.Vijaya Lakshmi
Designation : Asst. Professor
Department : Information Science and Engineering
Institution : Vemana Institute Of Technology
Educational Qualification : M.E

PG - [Computer Science], [Anna University], [2013]
UG - [Computer Science & Engg.], [VTU], [2005]

Research Interests :

1. Artificial Intelligence
2. Machine Learning

Experience :

Academic - 17 Years

Publications :

International Journals	-	07
National Journals	-	1
International Conferences	-	06
National Conferences	-	10

Professional Memberships :

Life Member	-	ISTE-Professional Membership.
Memberships	-	The Indian society for Technical Education

Achievements / Awards/ Recognition :

1. Handled the sessions on **"Programming Skills in C-Language"** in 2014 at SSEC, Anekal for placements.
2. Invited as a session Head for **reviewing Papers by International Journal of Latest Technology in Engineering, Management and Applied Science"** in 2015 at Sri Sai Ram college of Engineering.
3. Invited as internal Judge for handling sessions of 11th ISTE Karnataka State Student Convention "organized at SSEC on 30th April 2013.
4. Conducted Webinar on "Software and it's applications", as a resource person for Sir M. Visveswaraiah Institute of Technology, Bangalore on 17-3-2022.
5. **Worked as a journal reviewer at** Kristu Jayanti College, Bengaluru has organized IEEE Technically sponsored International Conference on Current Trends in Advanced Computing (ICCTAC–2024) during 8-9 May 2024 in association IEEE Bangalore Section.

Workshops / FDPs Attended :

1. Attended **AICTE sponsored 1 week FDP on "faculty Skills Enrichment"** fro20th Jan to 25th Jan 2014 in SSEC –Anekal.
2. Participated in **AICTE Sponsored 6 Days, Staff Development Program on "Intelligent E-Technology & Web Computing"** from 10th Dec to 15th Dec 2012 organized by CSE Dept. in SSEC.

3. Attended 5 days of **Infosys Campus Connect "Soft Skills training" workshop** in SJCIT- Chickballapur from Dec 17th to Dec 21st 2007 .
4. Attended **Staff Development Program, AICTE Sponsored on "Mobile Networks"** at Sri Sai Ram Engineering College-Chennai from 19th Jan-30th Jan 2009.
5. Attended one day Workshop on **"Microprocessor Interfacing" h**eld on April 2nd ,2009 conducted by CSE dept. at Auden Technology And Mgt. Academy, Bangalore.
6. Attended **AICTE sponsored 2 week FDP on "Visual System Programming"** On 23rd Sep to 7th October 2013 Organized by Dept. of ECE at Sri Sai Ram college of Engineering, Bangalore.
7. Attended **AICTE sponsored 1 week FDP on "faculty Skills Enrichment"** from 20th Jan to 25th Jan 2014 in SSEC –Anekal.
8. Attended a Faculty Development Program on **"IOT and its Applications"** on 30th Nov-2018" Organized by Er. Perumal Manimekalai College Of Engineering, Tamilnadu, in association with IEEE.
9. Attended a three days Faculty Development Program on **"ICT Tools and Effective Research"** organized by Department of Computer Science and Engineering held from 28th to 30th January 2019 in AMC College of Engineering, Bannerghatta Road, Bangalore.
10. Attended a 2-days Workshop conducted by IET on **"Outcome Based Education"** on Jan 17th and 18th 2019 in ALLIANCE UNIVERSITY.
11. Attended three days FDP program on "Data science with Python, from 6th Jan to 8th Jan 2020 conducted by ICT academy at KLE society Degree college, Nagarbavi, Bangalore
12. Attended a five days online Faculty Development Program on "A Devops Approach: Dockers in blockchain" held from 17th June to 21st June 2020 at AMC Engineering College Bangalore.
13. Participated in FDP on "Advances in Artificial Intelligence and Machine Learning", by The Oxford College of Engineering, Bangalore from 26-31 July 2021.
14. Attended FDP on "Online symposium on Emerging Technologies " in Collabaration with IBM Pvt Limited at IBM-Dr. AIT (Ambedkar Institute Of Technology) Bangalore from 28/3/22 to 1/4/22(5 days).
15. Mrs. R. Vijaya Lakshmi attended One day Intellectual Property on Patent filling, Trademark and Industrial Design, Graphical Indications, at Sahayadri College Of Engineering and Management, Mangaluru on World IP Day, 26th April,2022 ,in association with Visvesvaraya Trade Promotion centre (VIPC)(KSCST).
16. Attended Workshop on IP Protection in India, Patent drafting and filing process from 13/2/23 to 15/2/23 at Vemana Institute of Technology- KSCST IP Cell in association with ministry of MSME, Govt. of India.
17. Attended 5 days FDP on "Exploring Frontiers in AI Computing and Technologies "from 21.03. 2024 To 25.03.2024 at Online, Kingston Engineering College, Vellore .
18. Attended 6 days FDP on "Recent Technological Trends" from 1.04.2024 to 06.04. 2024 at Online, Eswari Engineering College, Ramapuram, Chennai Recent Technological Trends.
19. Attended workshop on Outcome Based Education and Accreditation Process at Vemana IT, in association with Dr. Ambedkar Institute of Technology, under AICTE Margdarshan scheme on 08-09-23 to 10-09-23.
20. FDP on Empowering Vision and Voice Through Generative AI and LLM at Alliance University from 5/5/25 to 9/5/25.

Lead Roles / Responsibilities :

Coordinator - [R and D Committee]

Member - [Guest Lecture/Magazine Co-ordinator]

Contact : [r.vijayalakshmi@vemanait.edu.in]